



Docket No.: 060188-00

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277
Makoto FUJIWARA : Confirmation Number: 5700
Application No.: 09/867,766 : Group Art Unit: 2134
Filed: May 31, 2001 : Examiner: Poltorak, Piotr

For: SEMICONDUCTOR INTEGRATED CIRCUIT AND METHOD OF TESTING
SEMICONDUCTOR INTEGRATED CIRCUIT

PETITION FOR EXTENSION OF TIME

Mail Stop Petition for Extension of Time
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

It is respectfully requested that the time for response to the Office Action dated November 4, 2005, now due to expire February 4, 2005, be extended for two month(s) and set to expire on April 4, 2005.

Please charge the extension fee of \$450.00 to Deposit Account No. 500417. Please charge any additional fees or credit any overpayment to Deposit Account No. 500417.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP

Michael E. Fogarty
Registration No. 36,139

**Please recognize our Customer No. 20277
as our correspondence address.**

03/29/2005 JADD01 00000097 500417 09867766
01 FC:1252 450.00 DA

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Date: March 28, 2005